

FIG. 1 RELATED ART

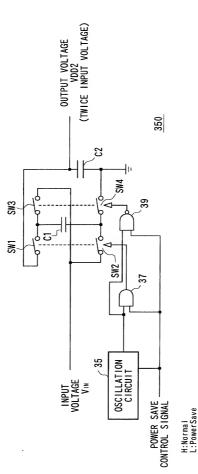
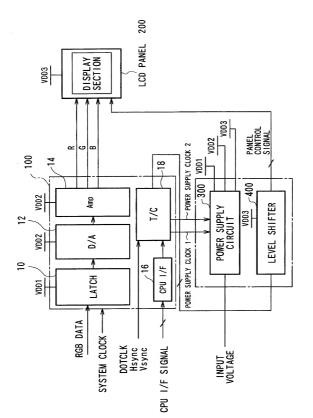
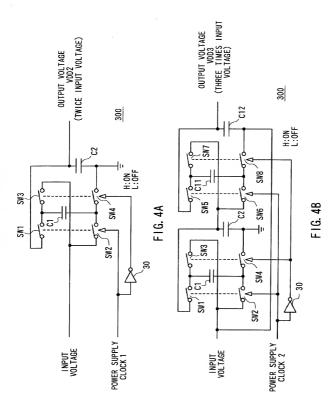


FIG. 2 RELATED ART



F1G. 3



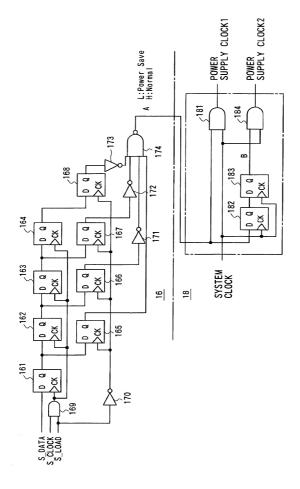
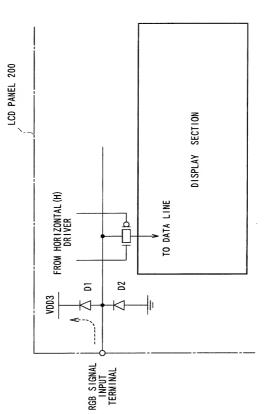


FIG. 5

• DATA = 0001 (4 BITS) (a) S_LOAD (b) S_CLOCK (c) S_DATA "0" (d) OUT169 (e) Q161 (f) Q162-164 (g) Q165 (h) Q166-168 (i) A (OUT174) H (j) B (Q183) (k) SYSTEM CLOCK (I) POWER SUPPLY CLOCK 1 ►>VDD3 down (m) POWER SUPPLY CLOCK 2 Normal → VDD2 down ├─ Power Save

FIG. 6



F1G. 7